

TFW 2123

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))				Docket No. 200-0664	
In Re Application Of: Joseph G. Walacavage et al.					
Application No. 09/965,905	Filing Date September 28, 2001	Examiner	Customer No. 33481	Group Art Unit 2123	Confirmation No. 4248
Title: METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM					
<p>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p>37 CFR 1.97(c)</p> <p>2. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p>OR</p> <p><input checked="" type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p>					

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(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
200-0664

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MAY 30 2006

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09/965,905	September 28, 2001		33481	2123	4248

Title: **METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM**

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 06-1510 as described below.
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May 25, 2006

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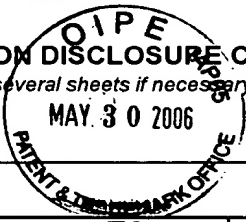
Signature

Dated: May 25, 2006

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Record I.D. 81082348

CC:

 <p>INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p>				ATTY DOCKET NO. 200-0664		APPLICATION NO. 09/965,905		
				APPLICANT(S) Joseph G. Walacavage et al.				
				FILING DATE September 28, 2001		GROUP ART UNIT 2123		
U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
U.S. PATENT APPLICATION PUBLICATIONS								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
			S. Kanai, T. Kishinami, "A Virtual Verification Environment for the Sequence Control System Using VRML and JAVA," 1999 by ASME, pages 1-8.					
			"ThermaView Advanced Welder Diagnostics System," 1998.					
EXAMINER				DATE CONSIDERED				

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.